

PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Yoshiaki Mori, et al.

Group Art Unit:

Serial No.:

10/026,286

Examiner: Not Yet Assigned

Filed:

December 20, 2001

Title:

Pattern Forming Method and Apparatus Used for Semiconductor

Device, Electric Circuit, Display Module, and Light Emitting Device

## CERTIFICATE OF MAILING

I hereby certify that this correspondence, is on this date, being deposited with the United States Postal Service with sufficient postage as "First Class" mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: September 18, 2003

## STATUS REQUEST

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

It is respectfully requested that Applicant(s) attorneys be advised of the current status of the above-identified application.

Respectfully submitted,

Rosalio Haro

Registration No. 42,633

Please address all correspondence to: Epson Research and Development, Inc. Intellectual Property Department 150 River Oaks Parkway, Suite 225 San Jose, CA 95134 Customer No. 20178

Phone: (408) 952-6000 Facsimile: (408) 954-9058

Date: September 18, 2003